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BEIJING BOE OPTOELECTRONICS TECHNOLOGY

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TFT- LCD PRODUCT 0	July.15.09	July.15.09'

REVISION HISTORY

REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
)		Initial Release	July.15.09'	Chen Ming
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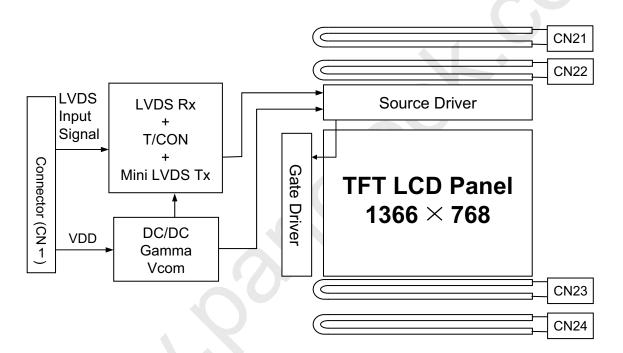
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1.0 GENERAL DESCRIPTION

1.1 Introduction

HT260WXC-100 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices with 4U-CCFLs backlight unit and L VDS interface (W/O Inverter). This module has a 26inch diagonally measured active area with HD resolutions (1366 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.2M colors(6bit+FRC colors).



1.2 Features

- LVDS Interface
- High-speed response
- Low power consumption
- 6-bit (FRC) color depth, display 16. 2M colors
- Incorporated edge type back-light (Four U-CCFL lamps)
- High luminance and contrast ratio, low reflection and wide viewing angle(160(H)/150(V) (CR>10) TN technology)

• RoHS Compliant

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1.3 Application

- Desktop Type of PC & Workstation Use
- Display Terminals for Control System
- For TV Use
- Display for Exhibition Show & Advertisement

1.4 General Specification

The followings are general specifications at the model HT260WXC-100.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	575.77(H) × 323.71(V)	mm	
Number of pixels	1366(H) ×768(V)	pixels	
Pixel pitch	$0.4215(H) \times 0.4215(V)$	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.2M	colors	
Display mode	Normally White		
Dimensional outline	$626(H) \times 373(V) \times 51.8(D)$ typ.	mm	
Weight	4150 (max.)	g	
Surface Treatment	Haze 25%, 3H		
Back-light	4 U-CCFL, type Direct		

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2.0 ABSOLUTE MAXIMUM RATINGS

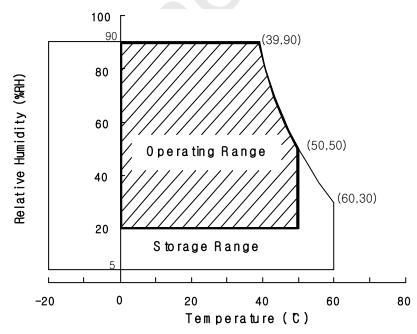
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings>

[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.3	6.0	V	
Logic Supply Voltage	V_{IN}	VSS-0.3	V _{DD} +0.3	V	Ta = 25 °C
Back-light Lamp Current	I_{BL}	3	8.0	mA	
Back-light Lamp frequency	F_L	40	80	kHz	
Operating Temperature	T_{OP}	0	+50	$^{\circ}$	1)
Storage Temperature	T_{ST}	-20	+60	$^{\circ}\mathbb{C}$	1)

Note: 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

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< Table 3. Electrical specifications >

[Ta = 25 ± 2 °C]

Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage V_{DD}		4.5	5.0	5.5	V	N. d
Power Supply Current	I_{DD}	-	460	750	mA	Note1
In-Rush Current	I_{RUSH}	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	V_{RF}	-	-	100	mV	$V_{DD} = 5.0V$
High Level Differential Input Threshold Voltage	V _{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	V _{IL}	-100	-	1	mV	
Differential input voltage	V _{ID}	200	-	600	mV	
Differential input common mode voltage	Vem	1.0	1.2	1.5		V_{IH} =100mV, V_{IL} =-100mV
Back-light Lamp Voltage	$V_{\rm BL}$	-	1870	-	V _{rms}	
Back-light Lamp Current	I_{BL}	7	7.5	8	mA _{rms}	
Back-light Lamp operating Frequency	F_{L}	40	-	80	KHz	Note 3
Lames Start Waltaga			-	3000	V _{rms}	25℃, Note 4
Lamp Start Voltage		-	1	2800	V _{rms}	0°C, Note 4
Lamp Life		40,000	50,000	-	Hrs	I _{BL} = 7.5 mA
	P _D	-	2.3	-	W	
Power Consumption	P_{BL}	-	56.1	-	W	I _{BL} =7.5 mA, Note 5
	P _{total}	-	58.4	-	W	

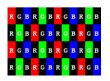
Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=5.0V, Frame rate=60Hz and

Clock frequency = 75.4 MHz. Test Pattern of power supply current

a) Typ: Color Bar pattern

b) Max: Skip Sub Pixel Pattern



- 2. Duration of rush current is about 2 ms and rising time of VDD is 520 μ s \pm 20 %
- 3. The lamp frequency should be selected as different as possible from the horizontal synchronous frequency and its harmonics to avoid interference, which may cause line flow on the display
- 4. The voltage above this value should be applied to the lamps for more than 1 second to start-up. Otherwise the lamps may not be turned on.
- 5. Calculated value for reference (V $_{BL}$ \times I $_{BL}$) \times 4 excluding inverter loss.

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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm 2\,^\circ\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of Θ and Φ equal to 0° . We refer to $\Theta_{\mathcal{O}=0}$ (= Θ_3) as the 3 o'clock direction (the "right"), $\Theta_{\mathcal{O}=90}$ (= Θ_{12}) as the 12 o'clock direction ("upward"), $\Theta_{\mathcal{O}=180}$ (= Θ_9) as the 9 o'clock direction ("left") and $\Theta_{\mathcal{O}=270}$ (= Θ_6) as the 6 o'clock direction ("bottom"). While scanning Θ and/or \mathcal{O} , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at $25\,^\circ\text{C}$. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

		[VDD =	= 5.0V, Frame rate	e = 60Hz, C	lock = 75.4	$MHz, I_{BL} =$	7.5mA, Ta	=25±2°C]
Parame	ter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	Θ_3		70	80	-	- Deg.	
Viewing Angle range	нопиопа	Θ_9	CR > 10	70	80	-	Deg.]
	374:1	Θ_{12}	CR > 10	70	80	-	Deg.]
	Vertical	Θ_6		60	70	-	Deg.	Note 1
Viewing Angle range	Horizontal	Θ_3		85	-	-	Deg.	Note 1
	нопиопа	Θ_9	CR > 5	85	-	-	Deg.	
	Vartical	Θ_{12}	CR > 3	85	-	-	Deg.	
	Vertical	Θ_6		85	-	-	Deg.	
Luminance Contrast ratio		CR)	500	800			Note 2
Luminance of White		Y _w		350	450		cd/m ²	Note 3
White luminance unif	ormity	ΔΥ		-	-	1.3		Note 4
	White	W_{x}	$\Theta = 0^{\circ}$ (Center) Normal Viewing Angle	⊖ = 0° Center) Normal 0	0.270	Typ. + 0.03		Note 5
	Winte	W_{y}			0.294			
	Red	R_x			0.633			
Reproduction	Red	R_y		Angle Typ.	0.333			
of color	Green	G_x		- 0.03	0.265			
	Green	G_y			0.590			
	Blue	B_x			0.141			
	Diuc	\mathbf{B}_{y}			0.058			
Response	Rising	$T_{\rm r}$		-	2.5	4	ms	Note 6
Time	Falling	T_{f}		-	5.5	8	ms	11010 0
Cross Ta	ılk	CT		-	-	2.0	%	Note 7

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Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 2. Contrast measurements shall be made at viewing angle of θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

CR = Luminance when displaying a white raster

Luminance when displaying a black raster

- 3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = ($ Minimum Luminance of 5points / Maximum Luminance of 5points) * 100 (See FIGURE 2 shown in Appendix).
- 5. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 3 shown in Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td, and 90% to 10% is Tr.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

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INTERFACE CONNECTION.

5.1 Electrical Interface Connection

Module Side Connector: JAE FI-X30SSL-HF or Equivalent • CN11 User Side Connector: JAE FI-X30H or Equivalent

Pin No.	Symbol	Function	Remark
1	NC	No connection	
2	NC	No connection	Internal use
3	NC	No connection	Internal use
4	GND	GND Ground	
5	RX0-	Negative LVDS differential data input. Channel 0	
6	RX0+	Positive LVDS differential data input. Channel 0	
7	GND	Ground	
8	RX1-	Negative LVDS differential data input. Channel 1	
9	RX1+	Positive LVDS differential data input. Channel 1	
10	GND	Ground	
11	RX2-	Negative LVDS differential data input. Channel 2	
12	RX2+	Positive LVDS differential data input. Channel 2	
13	GND	Ground	
14	RXCLK-	Negative LVDS differential clock input.	
15	RXCLK+	Positive LVDS differential clock input.	
16	GND	Ground	
17	RX3-	Negative LVDS differential data input. Channel 3	
18	RX3+	Positive LVDS differential data input. Channel 3	
19	GND	Ground	
20	NC	Not connection, this pin should be open.	
21	SELLVDS	"H" or NC for VESA, "L" for JEIDA	
22	NC	Not connection, this pin should be open.	
23	GND	Ground	
24	GND	Ground	
25	GND	Ground	
26	VCC	5.0V power supply	
27	VCC	5.0V power supply	
28	VCC	5.0V power supply	
29	VCC	5.0V power supply	
30	VCC	5.0V power supply	

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5.2 LVDS Interface (Tx; THC63LVDF83A or Equivalent) 5.2.1 LVDS Interface

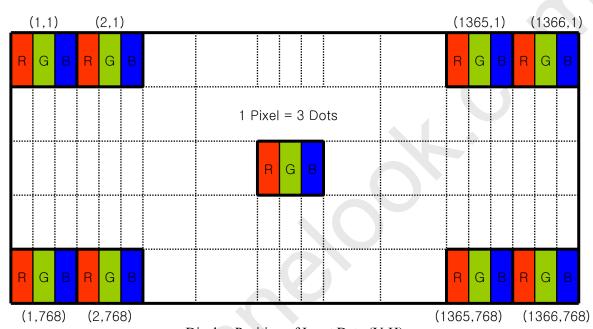
	Input	Transmitter		Inter	face	HT260WXC-100 (CN11)	Remark
	Signal	Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.	
	OR0	51					
	OR1	52					
	OR2	54	40	OLUTO	DVO		
	OR3	55	48 47	OUT0- OUT0+	RXO0- RXO0+	$\frac{1}{2}$	
	OR4	56	1,	0010	10100		
	OR5	3					
	OG0	4					
	OG1	6					
	OG2	7				Ť	
	OG3	11	4.6	OLUTI1	DVO1	2	
	OG4	12	46 45	OUT1- OUT1+	RXO1- RXO1+	3 4	
	OG5	14	13	00111	TCXO11	7	
	OB0	15					
	OB1	19	,				
L V	OB2	20					
Ď	OB3	22					
S	OB4	23		OT ITTO	DWGG	_	
	OB5	24	42 41	OUT2- OUT2+	RXO2- RXO2+	5 6	
	Hsync	27	71	00121	ICAO2	O	
	Vsync	28					
	DE	30					
	MCLK	31	40	CLK OUT-	RXO CLK-	8	
			39	CLK OUT+	RXO CLK+	9	
	OR6	50					
	OR7	2					
	OG6	8	38	OUT3-	RXO3-	10	
	OG7	10	37	OUT3+	RXO3+	10	
	OB6	16					
	OB7	18					
	RSVD	25					

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5.3 Data Input Format



Display Position of Input Data (V-H)

5.4 Back-light Interface Connection

●CN 21,22 Module Side Connector :Cvilux CP04040000or Equivalent
User Side Connector :CP042CP1HTO-LF or Equivalent

PIN NO.	INPUT	COLOR	FUNCTION
1	НОТ	Pink or Blue	High Voltage
2	COLD	White or Gray	Ground

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6.0 SIGNAL TIMING SPECIFICATION

 $6.1\ The\ HT260WXC\text{-}100$ is operated by the DE only.

	Item		Min	Тур	Max	Unit	
	Frequency	1/Tc	50	75.4	82	MHz	
Clock	High Time	Tch	-	4/7Tc	-		
	Low Time	Tcl	-	3/7Tc	-		
			778	806	888	lines	
F1	rame Period	Tv	Tv	50	60	63	Hz
			15.9	16.7	20	ms	
Vertica	al Display Period	Tvd	-	768	-	lines	
One line Scanning Period		Th	1446	1560	1936	clocks	
Horizontal Display Period		Thd	-	1366	-	clocks	
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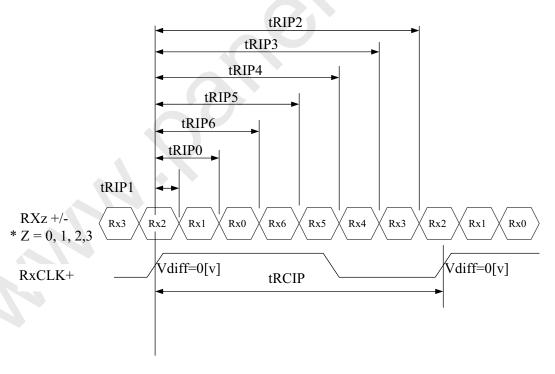
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6.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 4.

<Table 4. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	12.20	13.26	20.00	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 ×tRCIP/7-0.4	2 ×tRCIP/7	$2 \times tRCIP/7+0.4$	nsec	
Input Data 3	tRIP5	3 ×tRCIP/7-0.4	3 ×tRCIP/7	3 ×tRCIP/7+0.4	nsec	
Input Data 4	tRIP4	4 ×tRCIP/7-0.4	4 ×tRCIP/7	$4 \times tRCIP/7+0.4$	nsec	
Input Data 5	tRIP3	5 ×tRCIP/7-0.4	5 ×tRCIP/7	$5 \times tRCIP/7+0.4$	nsec	
Input Data 6	tRIP2	$6 \times \text{tRCIP/7-0.4}$	6 ×tRCIP/7	$6 \times tRCIP/7+0.4$	nsec	



* Vdiff = (RXz+)-(RXz-),...,(RXCLK+)-(RXCLK-)

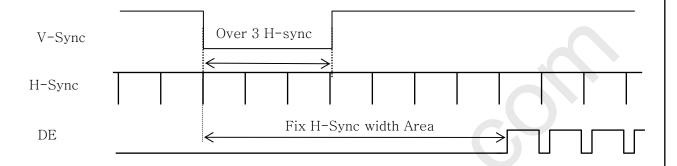
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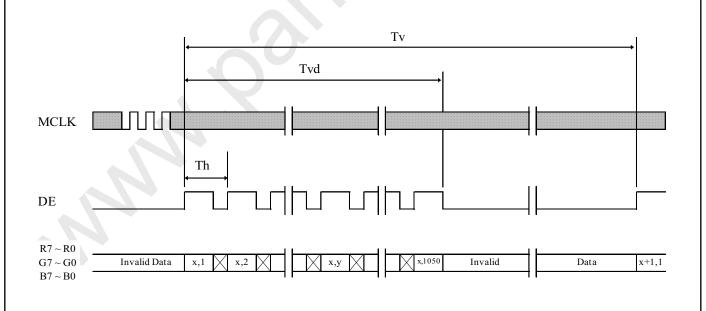
7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

7.1 Sync Timing Waveforms



- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

7.2 Vertical Timing Waveforms

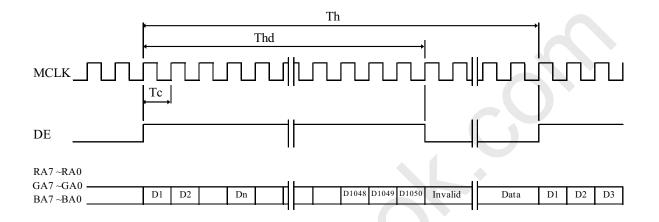


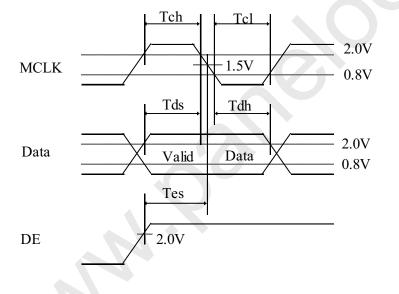
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7.3 Horizontal Timing Waveforms





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8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

				RI	ED I	DA	ГΑ					GRI	EEN	I DA	\TA					BL	UE	DA	TA		
Color & C	ray Scale	R7	R6		R4			R1	R0	G7							G0	В7	В6	В5				В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0.	0	0	0	0	0
D : G 1	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0,	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	Δ				,	1															,	\uparrow			
of RED	∇																					<u> </u>			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of GREEN	\triangle		1						↑							<u> </u>									
OI GILLEIN	∇				,																	<u> </u>			
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	Δ					<u> </u>																<u> </u>			
OI BECE	∇				,	_							,								,	<u> </u>			
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
of WHITE	Δ	_																				<u> </u>			
or willing	∇	<u> </u>			,			-		_	_		<u>, </u>			_						ļ .		-	
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

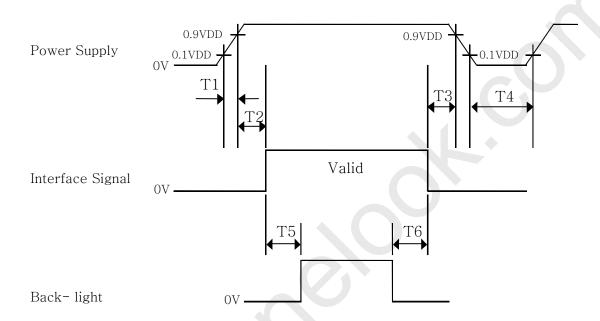
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9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- \bullet 0.5 ms \leq T1 \leq 10 ms
- $0 \le T2 \le$ 50 ms
- $0 \le T3 \le 50 \text{ ms}$
- $1 \text{ sec } \leq \text{T4}$
- $200 \text{ ms} \leq T5$
- $200 \text{ ms} \leq T6$

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.

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10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model HT260WXC-100. Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	626×373×51.8	mm
Weight	4150 (max.)	gram
Active area	575.77 (H) × 323.71 (V)	mm
Pixel pitch	$0.4215 \text{ (H)} \times 0.4215 \text{ (V)}$	mm
Number of pixels	$1366 \text{ (H)} \times 768 \text{ (V) (1 pixel} = R + G + B \text{ dots)}$	pixels
Back-light	4 U-CCFL Direct type	

10.2 Mounting

See FIGURE 5. (shown in Appendix)

10.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below. <Table 6. Reliability Test Parameters >

No	Test Items		Conditions
1	High temperature storage test	$Ta = 60 ^{\circ}\text{C}, 240 \text{h}$	nrs
2	Low temperature storage test	Ta = -20 °C, 240	hrs
3	High temperature & high humidity operation test	$Ta = 50 ^{\circ}\text{C}, 80\%\text{I}$	RH, 240hrs
4	High temperature operation test	$Ta = 50 ^{\circ}\text{C}, 240\text{hz}$	rs
5	Low temperature operation test	$Ta = 0 ^{\circ}C$, 240hrs	3
6	Thermal shock	Ta = -20 °C \leftrightarrow 60 °C (0.5 hr), 100 cycle	
7	Vibration test (non-operating)	Frequency Gravity / AMP Period	Sine wave, 10 ~ 300 Hz, Sweep rate 30 min 1.5 G X, Y, Z 30 min
		Gravity	50G
8	Shock test (non-operating)	Pulse width	11msec, sine wave
		Direction	$\pm X$, $\pm Y$, $\pm Z$ Once for each
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330 Ω, 15 KV Contact : 150 pF, 330 Ω, 8 KV	

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12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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13.0 PRODUCT SERIAL NUMBER



- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001:01, 2002:02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number

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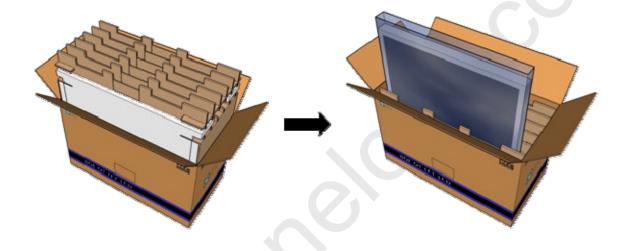
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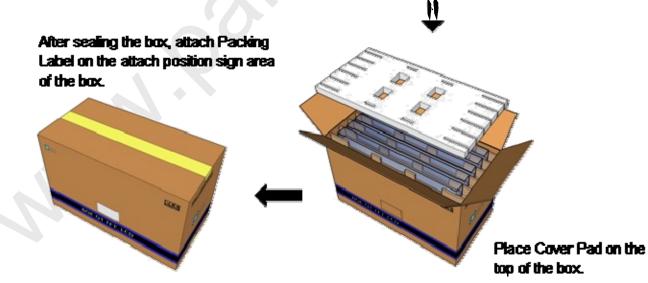
14.0 Packing

14.1 Packing Order

As shown in the figure, place the modules bundled by PE bag in the box.

Put Pad into the box.





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14.2 Packing Note

• Box Dimension : $718mm(W) \times 360mm(L) \times 455mm(H)$

• Package Quantity in one Box : 5 pcs

14.3 Box label

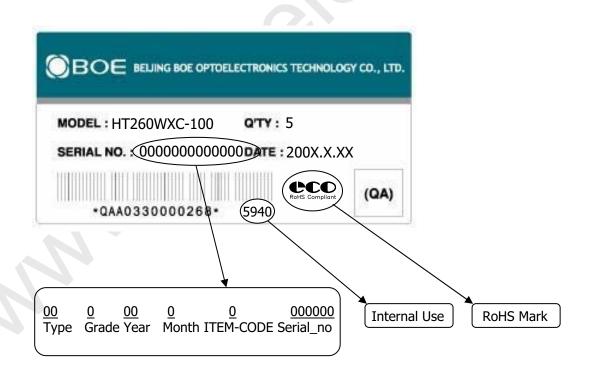
• Label Size : 108 mm (L) × 56 mm (W)

Contents

Model: HT260WXC-100 Q'ty: Module Q'ty in one box

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date



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15.0 APPENDIX

Figure 1. Measurement Set Up

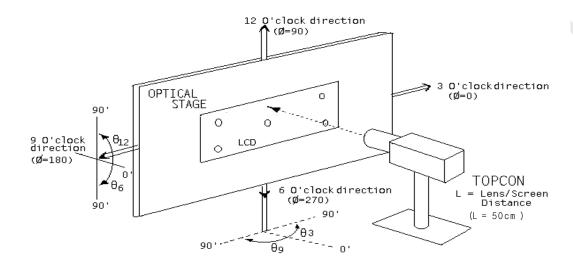
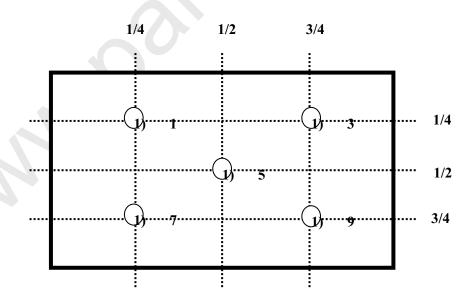


Figure 2. White Luminance and Uniformity Measurement Locations (5 points)



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Figure 3. Response Time Testing

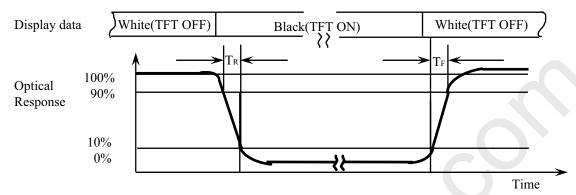
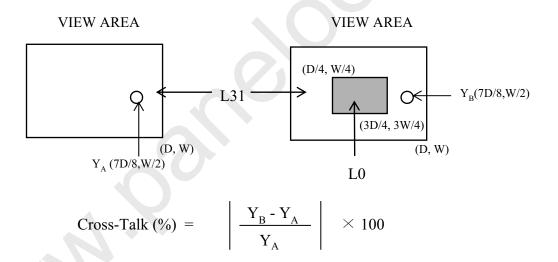
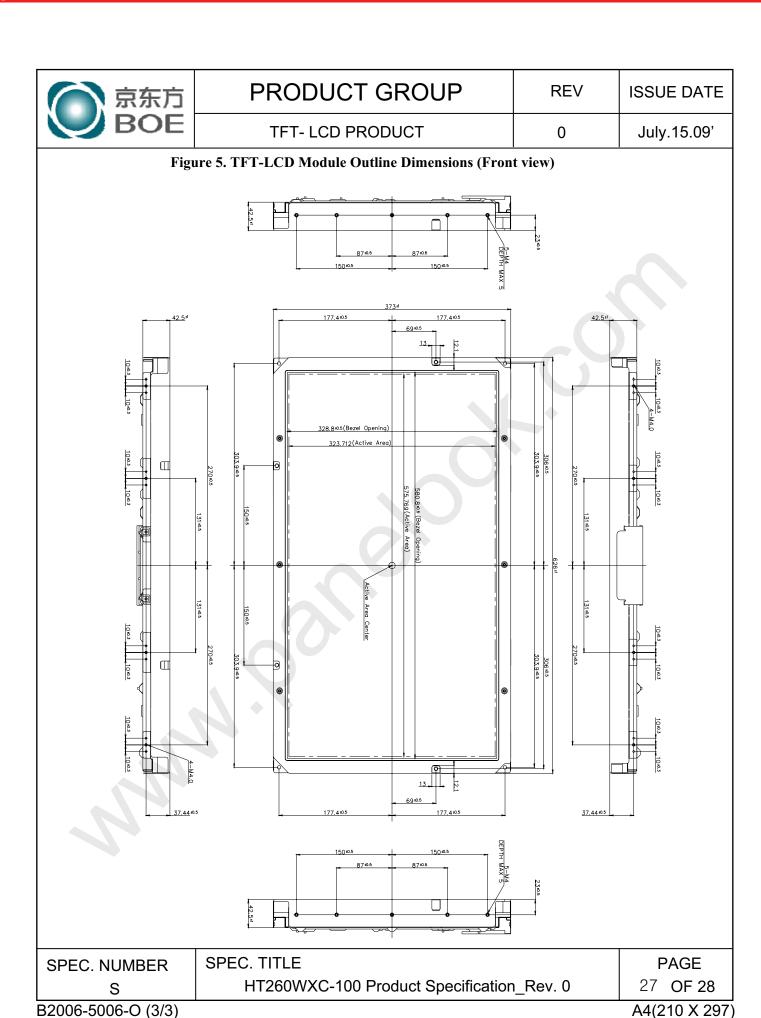


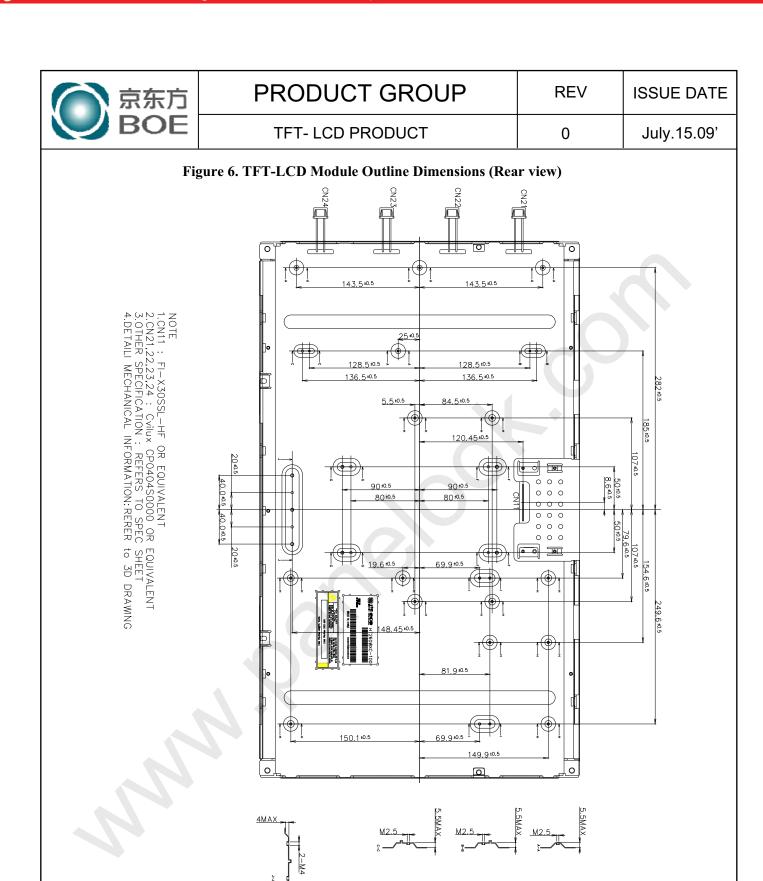
Figure 4. Cross Modulation Test Description



Where: $Y_A =$ Initial luminance of measured area (cd/m²) $Y_B =$ Subsequent luminance of measured area (cd/m²) The location measured will be exactly the same in both patterns

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